FIG. 1

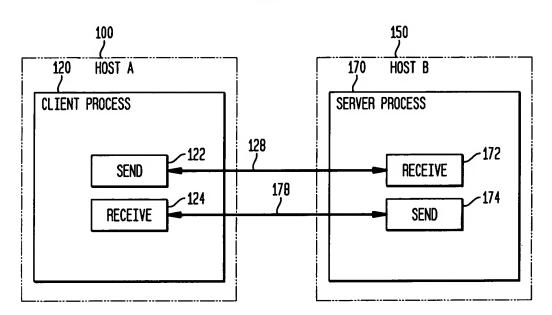
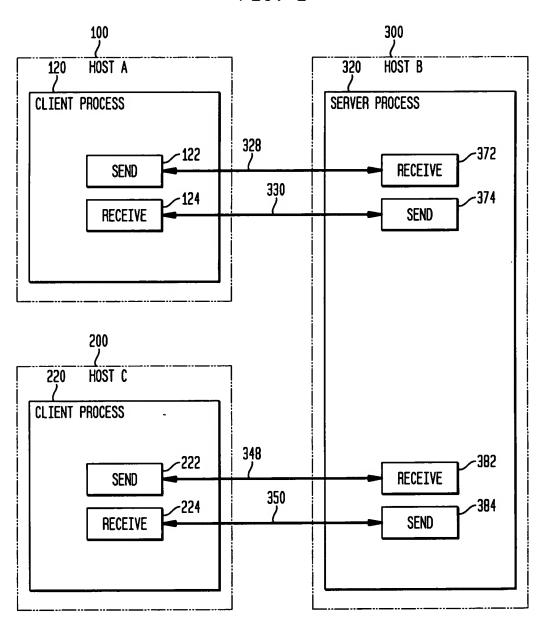


FIG. 2



A10

HOST

A40

A40

A20

NETWORK
INTERFACE

A30

MEMORY

A30

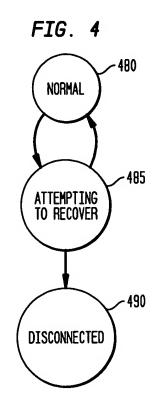


FIG. 5

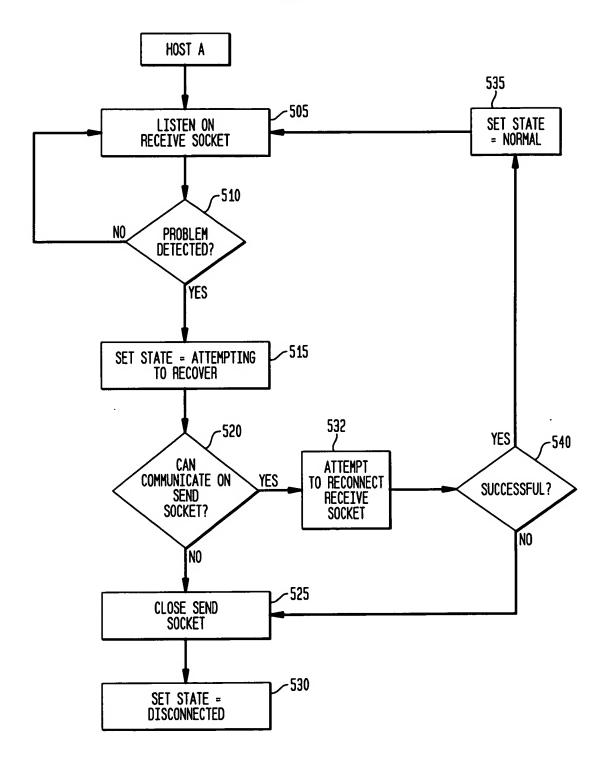


FIG. 6

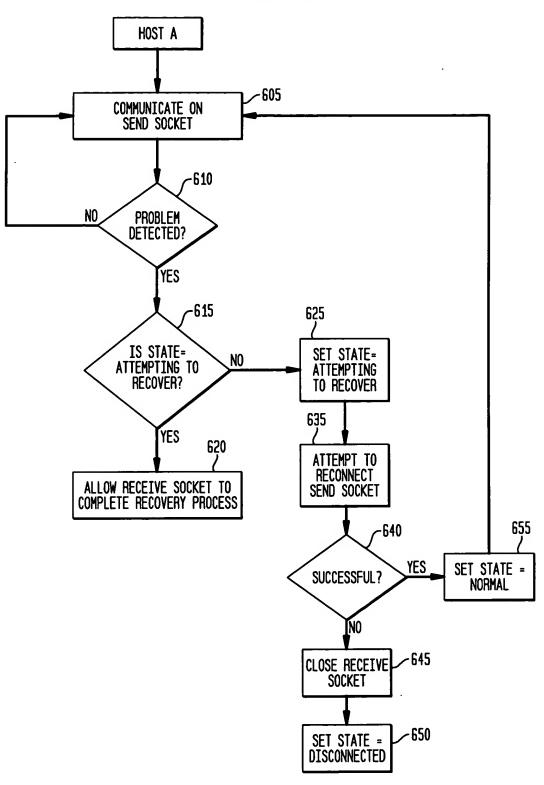


FIG. 7

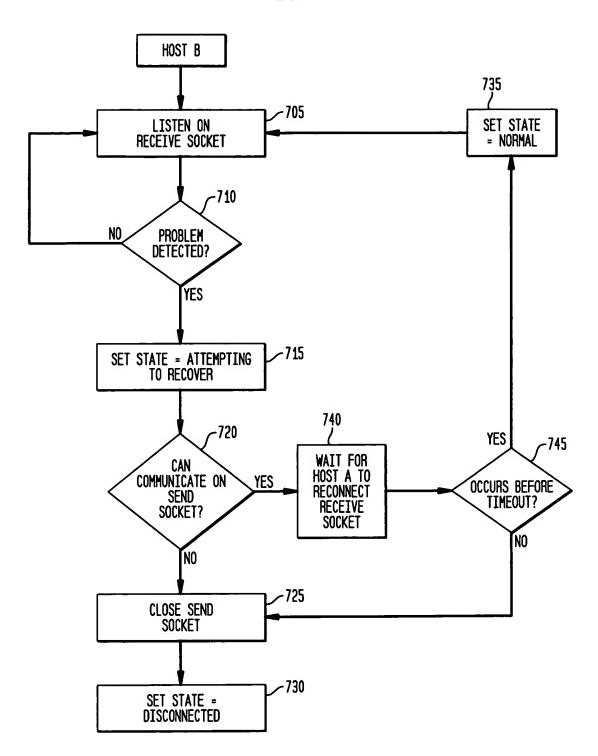


FIG. 8

